
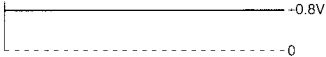
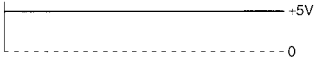

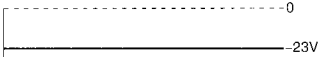
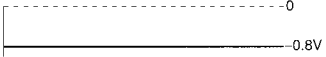
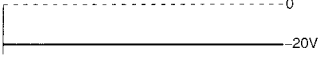
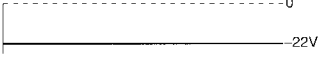







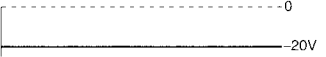



TP	Name	Scope	Freq. Per.	Data H/L/A	Description
222	NOT_ON		0	L	NOT ON
223	RAM_POWER		0	H	RAM POWER
224	+VRAM		0	H	+Supply Voltage for the RAM/...
233	V1		0	H	Power supply for LCD drive (+2.3V)
234	V2		0	L	Power supply for LCD drive (-23V)
235	V3		0	L	Power supply for LCD drive (-0.8V)
236	V4		0	L	Power supply for LCD drive (-20V)
237	V5		0	L	Power supply for LCD drive (-22V)
238	V6		0	H	Power supply for LCD drive (+0.8V)
239	μ PCLK		12.5 MHz	A	μ Processor clock, point 9 of D1201
240	GROUND				
241	BAT_LEVEL		0	H	BATtery LEVEL/analog channel 6, point 20 of D1201
244	RAMSELN			A	RAM SElect Not, point 20 of D1204
246	OPTSELN		500 kHz	A	Option Select Not
247	A15		2 MHz	A	.../ROM 1 select not
248	-20V		0	H	-20V supply
249	+5V		0	H	+5V supply